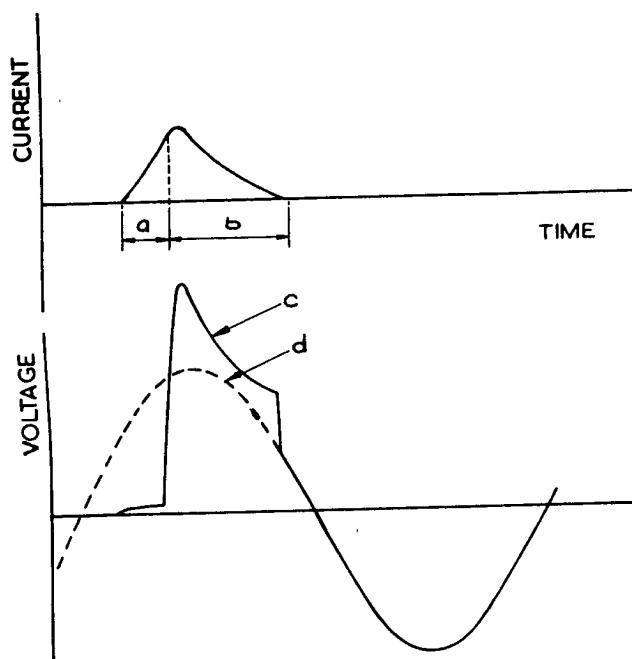


DRAWINGS



*Electrical conditions during short circuit*

- a* pre-arching period
- b* arcing period
- c* fuselink voltage
- d* source e.m.f.

FIG. 1.

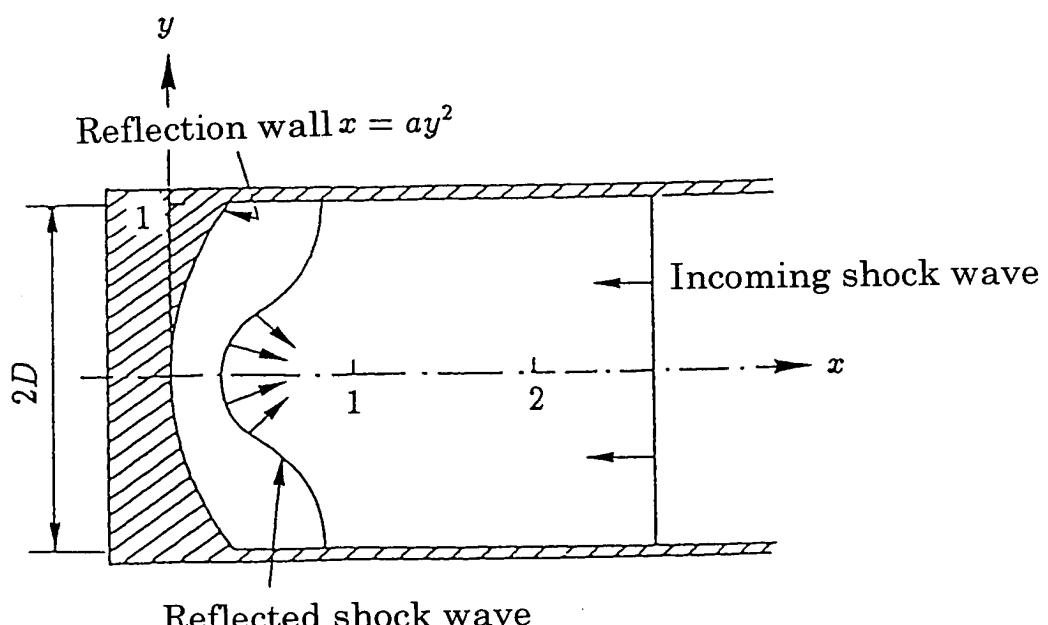


FIG. 2.

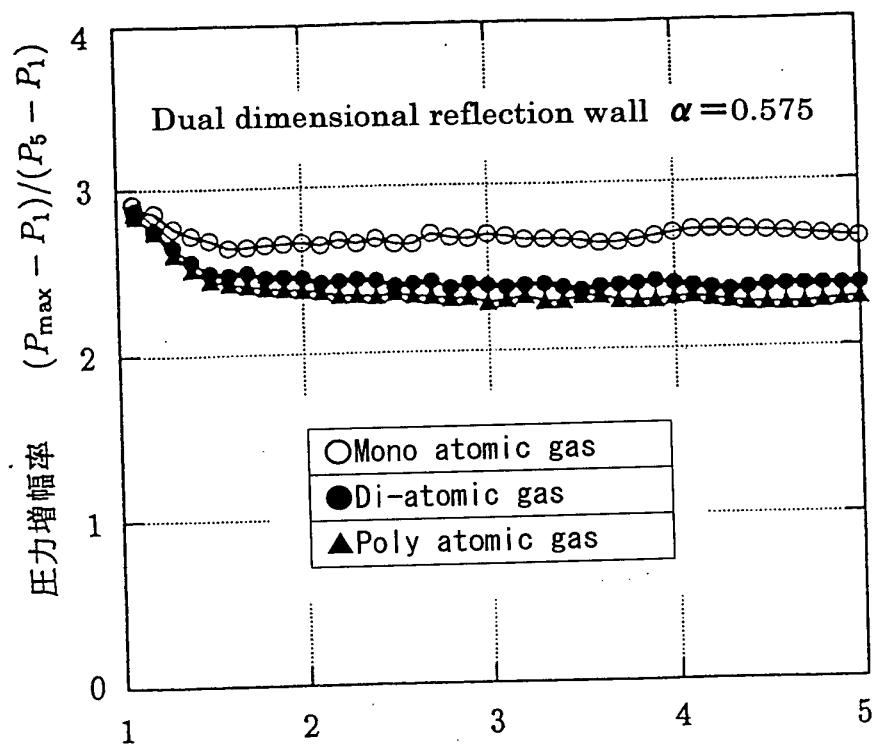


FIG. 3.

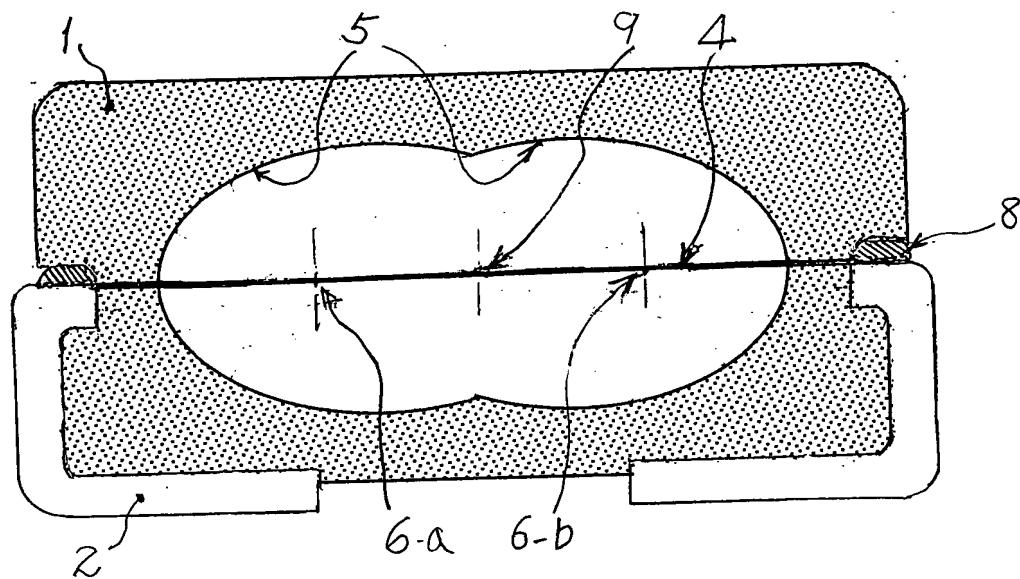


FIG. 4

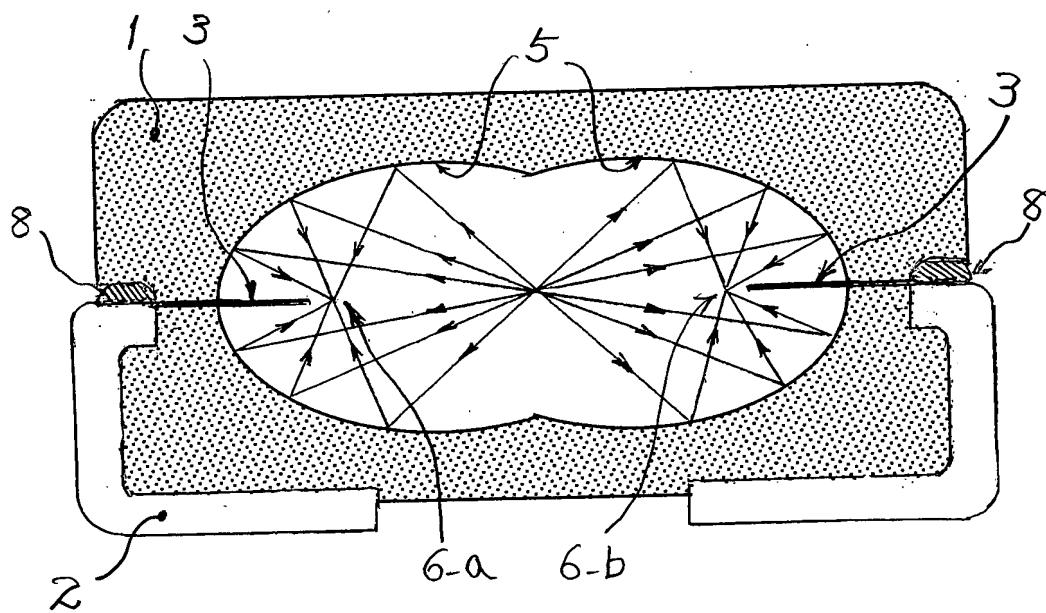


FIG. 5.

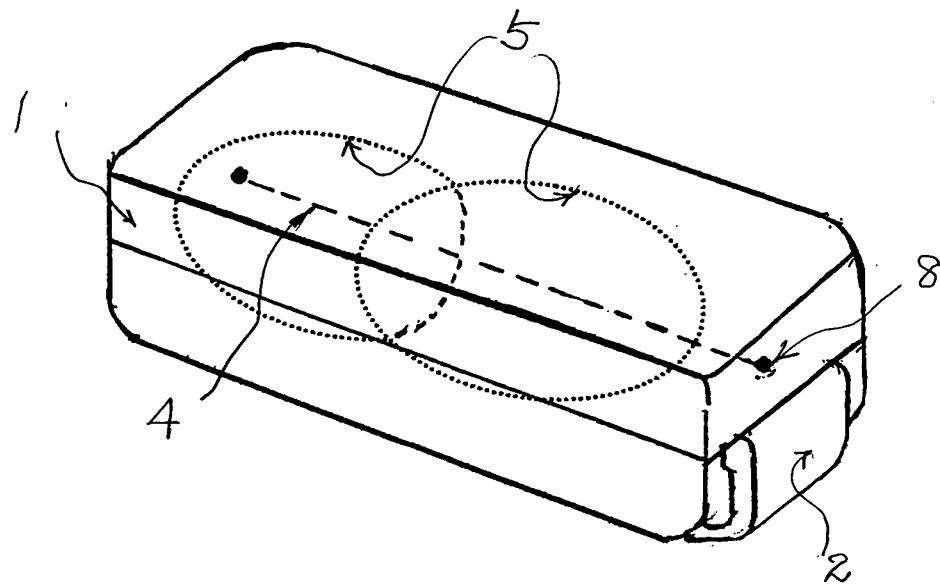


FIG. 6

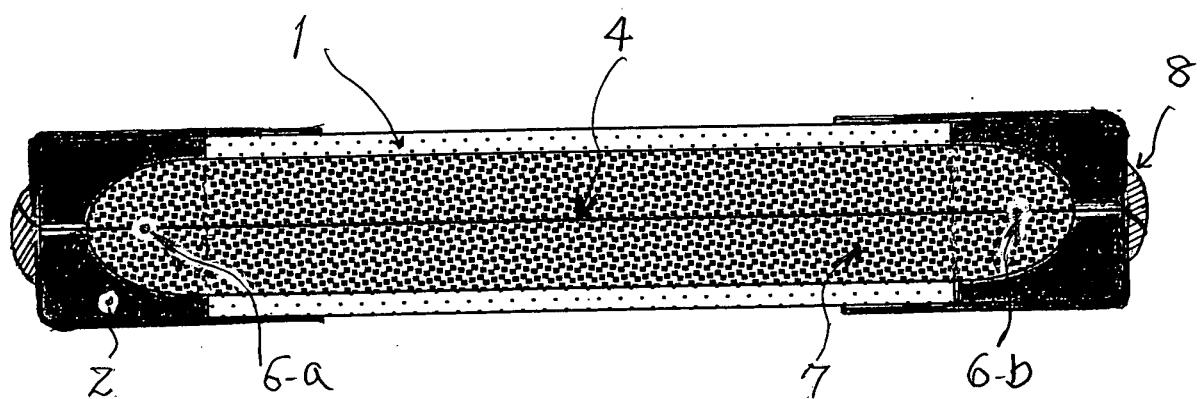


FIG. 7

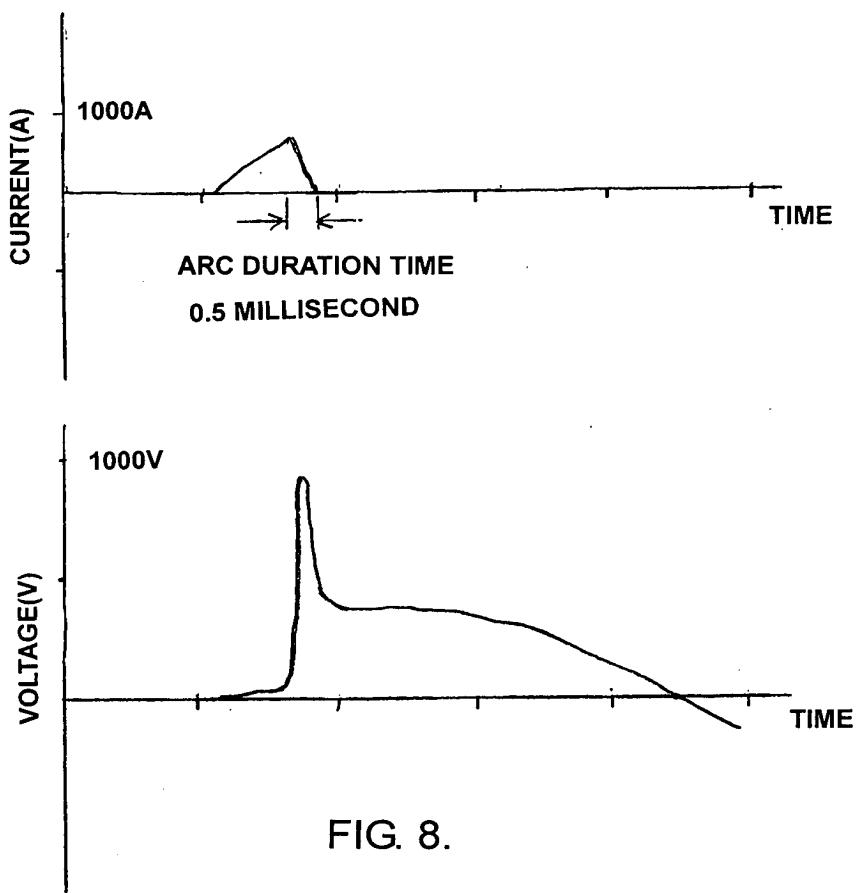


FIG. 8.

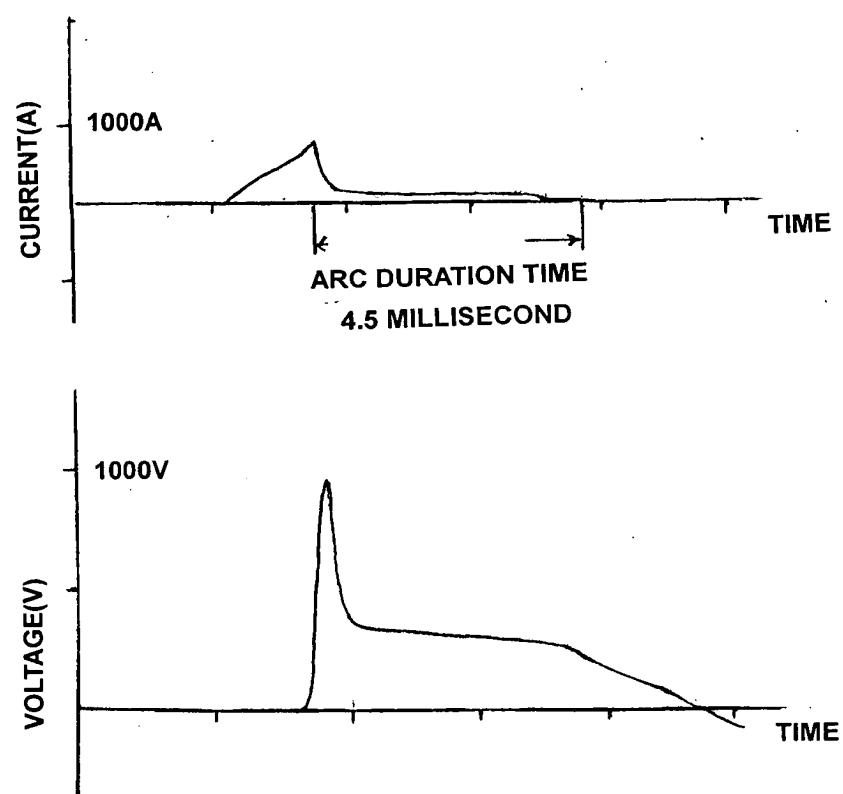


FIG. 9.